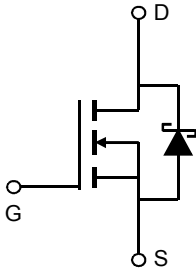
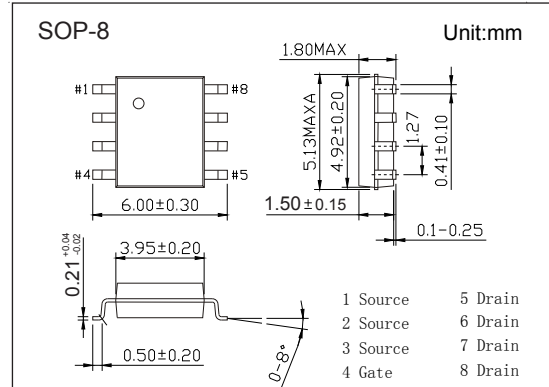


## N-Channel MOSFET

### AO4712 (KO4712)

#### ■ Features

- $V_{DS} = 30V$
- $I_D = 13 A$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 11m\Omega$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 14m\Omega$  ( $V_{GS} = 4.5V$ )
- SRFET™ Soft Recovery MOSFET: Integrated Schottky Diode



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous Drain Current	$I_D$	$T_A=25^\circ C$	13
		$T_A=70^\circ C$	10
Pulsed Drain Current	$I_{DM}$	68	A
Avalanche Current	$I_{AS}, I_{AR}$	15	A
Repetitive Avalanche Energy	$L=0.1mH$	$E_{AS}, E_{AR}$	11
Power Dissipation	$P_D$	$T_A=25^\circ C$	3.1
		$T_A=70^\circ C$	2
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	$t \leq 10s$	40
		Steady-State	75
Thermal Resistance.Junction- to-Lead	$R_{thJL}$	24	$^\circ C/W$
Junction Temperature	$T_J$	150	$^\circ C$
Storage Temperature Range	$T_{stg}$	-55 to 150	$^\circ C$

## N-Channel MOSFET

### AO4712 (KO4712)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V	30			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			0.5	mA	
		V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			100		
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	1.1		2.1	V	
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =13A			11	mΩ	
		V <sub>GS</sub> =10V, I <sub>D</sub> =13A, T <sub>J</sub> =125°C			16		
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =11A			14		
On State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V	68			A	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =13A		80		S	
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz	930		1400	pF	
Output Capacitance	C <sub>oss</sub>		90		170		
Reverse Transfer Capacitance	C <sub>rss</sub>		45		125		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	0.7		2.1	Ω	
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, I <sub>D</sub> =13A	16		24	nC	
Total Gate Charge (4.5V)			7		10.5		
Gate Source Charge			Q <sub>gs</sub>		3.2		
Gate Drain Charge			Q <sub>gd</sub>		3		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, R <sub>L</sub> =1.2Ω, R <sub>GEN</sub> =3Ω		6		ns	
Turn-On Rise Time	t <sub>r</sub>			2.4			
Turn-Off DelayTime	t <sub>d(off)</sub>			23			
Turn-Off Fall Time	t <sub>f</sub>			4			
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =13A, di/dt=500A/us	5.5		8.5	nC	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		5		8		
Maximum Body-Diode Continuous Current	I <sub>S</sub>				5	A	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V			0.7	V	

Note. The static characteristics in Figures 1 to 6 are obtained using <300 us pulses, duty cycle 0.5% max.

#### ■ Marking

Marking	4712
	KC****

## N-Channel MOSFET AO4712 (KO4712)

■ Typical Characteristics

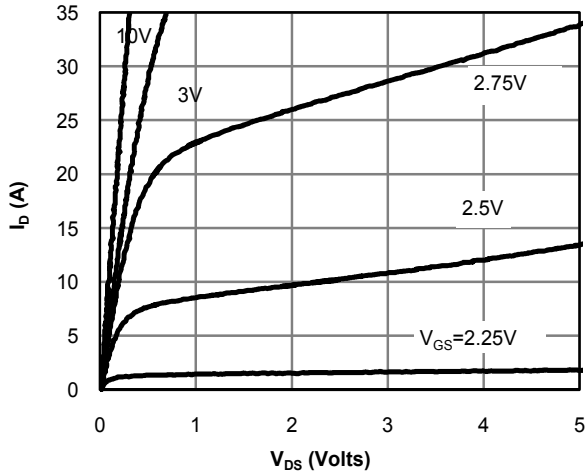


Fig 1: On-Region Characteristics (Note E)

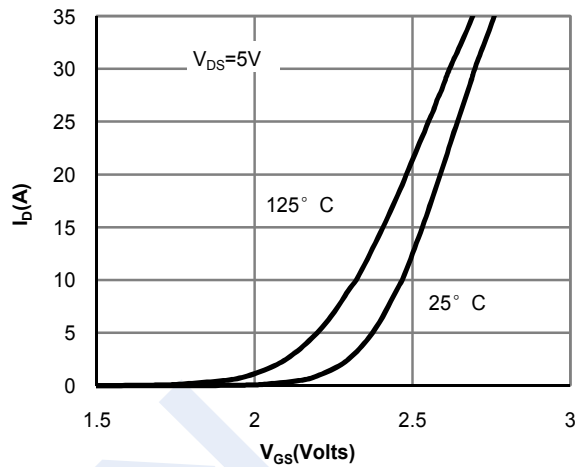


Figure 2: Transfer Characteristics (Note E)

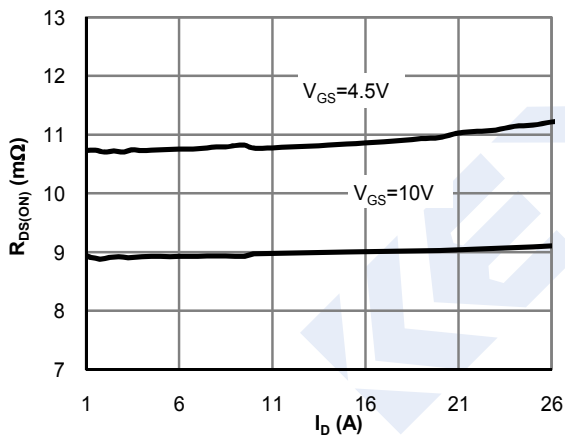


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

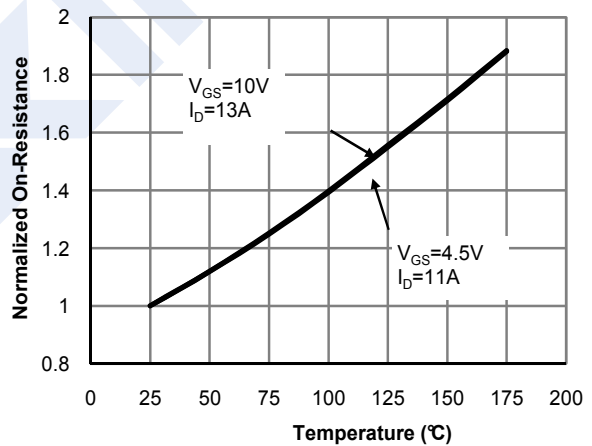


Figure 4: On-Resistance vs. Junction Temperature (Note E)

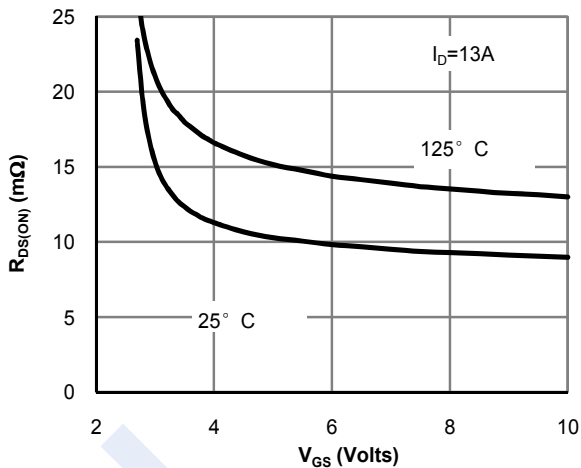


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

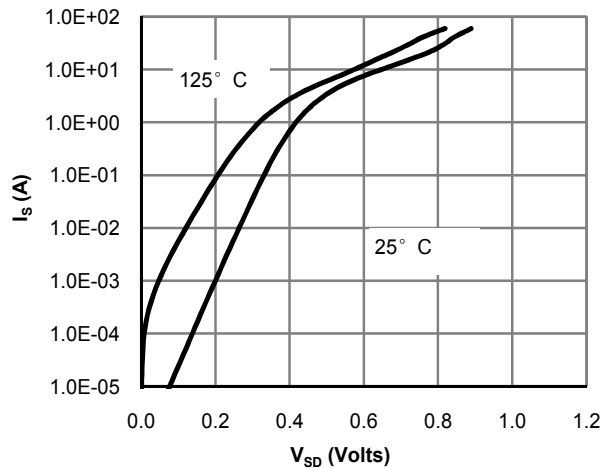


Figure 6: Body-Diode Characteristics (Note E)

## N-Channel MOSFET AO4712 (KO4712)

■ Typical Characteristics

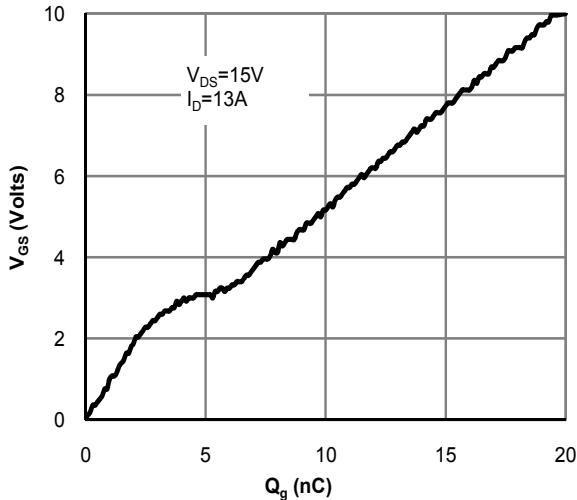


Figure 7: Gate-Charge Characteristics

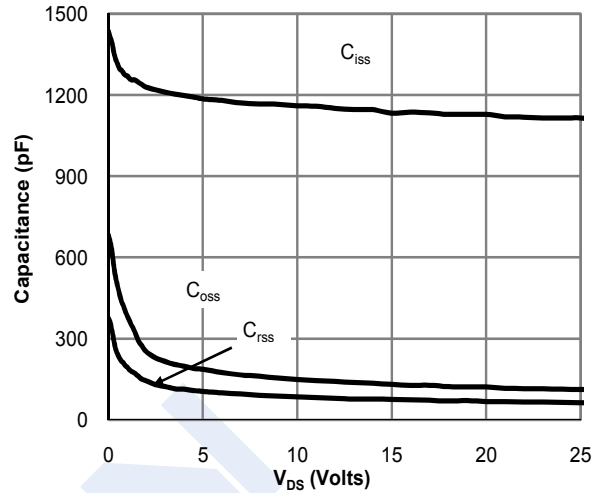


Figure 8: Capacitance Characteristics

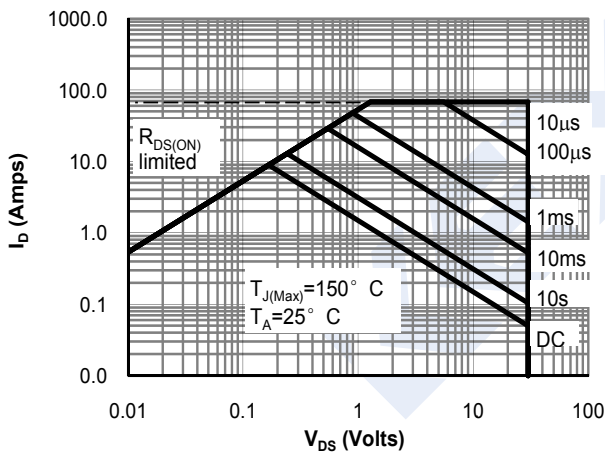


Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

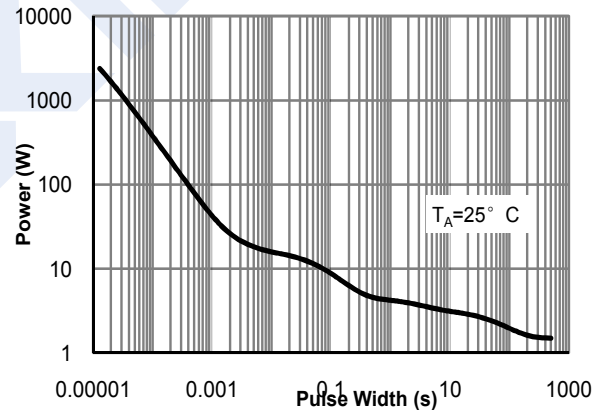


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)

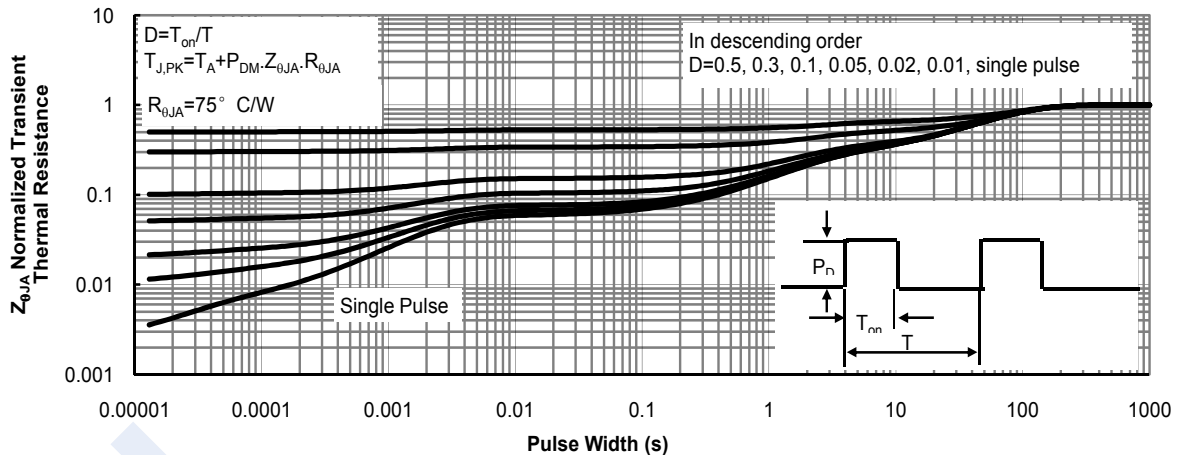


Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)